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L1	320	(digital adj signal adj processor).ti.	USPAT	OR	ON	2005/06/09 14:27
L2	25	1 and (first adj stage) and (second adj stage)	USPAT	OR	ON	2005/06/09 14:30
L3	6	2 and latency	USPAT	OR	OFF	2005/06/09 14:30

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	2992	((703/19) or (379/88.07) or (340/855.5) or (716/8,2,6,12,18)). CCLS.	USPAT	OR	OFF	2005/06/09 13:57
L2	4	1 and (first adj2 stage) and (second adj2 stage) and DSP	USPAT	OR	ON	2005/06/09 14:00
L3	2	1 and z-domain	USPAT	OR	ON	2005/06/09 14:00

Results for "((no <near/2> latency)<in>metadata)"

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- ☐ **1. Bit-serial multipliers and squarers**
Ienne, P.; Viredaz, M.A.;
Computers, IEEE Transactions on
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[AbstractPlus](#) | Full Text: [PDF\(420 KB\)](#) IEEE JNL
- ☐ **2. A nonblocking algorithm for shared queues using compare-and-swap**
Prakash, S.; Yann Hang Lee; Johnson, T.;
Computers, IEEE Transactions on
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[AbstractPlus](#) | Full Text: [PDF\(920 KB\)](#) IEEE JNL
- ☐ **3. Theory and applications of incremental /spl Delta/spl Signal/ converters**
Markus, J.; Silva, J.; Temes, G.C.;
Circuits and Systems I: Regular Papers, IEEE Transactions on [see also Circuits and Systems I: Fundamental Theory and Applications, IEEE Transactions on]
Volume 51, Issue 4, April 2004 Page(s):678 - 690
[AbstractPlus](#) | [References](#) | Full Text: [PDF\(416 KB\)](#) IEEE JNL
- ☐ **4. Parallel turbo coding interleavers: avoiding collisions in accesses to storage elements**
Giulietti, A.; van der Perre, L.; Strum, A.;
Electronics Letters
Volume 38, Issue 5, 28 Feb. 2002 Page(s):232 - 234
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- ☐ **5. Receiver design for MIMO-OFDM transmission over time variant channels**
Al-Naffouri, T.Y.; Awoniyi, O.; Oteri, O.; Paulraj, A.;
Global Telecommunications Conference, 2004. GLOBECOM '04. IEEE
Volume 4, 29 Nov.-3 Dec. 2004 Page(s):2487 - 2492 Vol.4
[AbstractPlus](#) | Full Text: [PDF\(576 KB\)](#) IEEE CNF
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- ☐ **7. Arithmetic structures for inner-product and other computations based on a latency-free bit-serial multiplier design**
Haynal, S.; Parhami, B.;
Signals, Systems and Computers, 1996. 1996 Conference Record of the Thirtieth Asilomar Conference on
Volume 1, 3-6 Nov. 1996 Page(s):197 - 201 vol.1

- ☐ **8. A 15 mW 1.6 Gb/s wormhole data router for 2-D meshes**
Yen-Wen Lu; Yeh, G.K.; Burr, J.B.;
VLSI Circuits, 1996. Digest of Technical Papers., 1996 Symposium on
13-15 June 1996 Page(s):138 - 139
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- ☐ **9. Decoupled vector architectures**
Espasa, R.; Valero, M.;
High-Performance Computer Architecture, 1996. Proceedings. Second International Symposium on
3-7 Feb. 1996 Page(s):281 - 290
[AbstractPlus](#) | Full Text: [PDF\(972 KB\)](#) [IEEE CNF](#)

- ☐ **10. Low-energy encoding for deep-submicron address buses**
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Low Power Electronics and Design, International Symposium on, 2001.
6-7 Aug. 2001 Page(s):176 - 181
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- ☐ **11. Design theory for high-order incremental converters**
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Intelligent Signal Processing, 2003 IEEE International Symposium on
4-6 Sept. 2003 Page(s):3 - 8
[AbstractPlus](#) | Full Text: [PDF\(433 KB\)](#) [IEEE CNF](#)

- ☐ **12. Parallel high-radix nonrestoring division**
Schwarz, E.M.; Flynn, M.J.;
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Volume 42, Issue 10, Oct. 1993 Page(s):1234 - 1246
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Cummings, A.J.; Cahill, L.W.;
Communications, IEEE Transactions on
Volume 46, Issue 5, May 1998 Page(s):584 - 586
[AbstractPlus](#) | [References](#) | Full Text: [PDF\(56 KB\)](#) [IEEE JNL](#)

- ☐ **14. Effects of Network Characteristics on Task Performance in a Desktop CVE System**
Ling Chen;
Advanced Information Networking and Applications, 2005. AINA 2005. 19th International Conference on
Volume 1, 25-30 March 2005 Page(s):821 - 826
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- ☐ **15. Modeling the technology impact on the design of a two-level multicomputer interconnection network**
Cruz-Rivera, J.L.; Wills, S.; Gaylord, T.; Glytsis, E.;
Computer Design: VLSI in Computers and Processors, 1996. ICCD '96. Proceedings., 1996 IEEE
International Conference on
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- ☐ **16. Design of Giga-Bit/s optical interconnection network for computer cluster**
Ge Zhou; Wen-Cai Jing; Yi-Mo Zhang; Feng Hua; Jin-Dong Tian; Xian-Ling Chen; Wei Liu;
Massively Parallel Processing, 1998. Proceedings. Fifth International Conference on
15-17 June 1998 Page(s):194 - 199
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- ☐ **17. Dynamic termination output driver for a 600 MHz microprocessor**
Vishwanthaiah, S.; Ang, M.; Starr, J.; Taylor, A.;
Solid-State Circuits Conference, 2000. Digest of Technical Papers. ISSCC. 2000 IEEE International
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18. Congestion control of ATM networks using a learning methodology

Jagannathan, S.; Tohmaz, A.;
Control Applications, 2001. (CCA '01). Proceedings of the 2001 IEEE International Conference on
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19. Breaking the memory bottleneck with an optical data path

Fritts, J.E.; Chamberlain, R.D.;
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Terms used without regard to latency

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1 [Instruction scheduling for the HP PA-8000](#)



David A. Dunn, Wei-Chung Hsu

December 1996 **Proceedings of the 29th annual ACM/IEEE international symposium on Microarchitecture**

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The PA-8000 is capable of reordering independent operations at run time, a task normally performed only by the instruction scheduler in the compiler. This paper presents some of the unique issues faced by an instruction scheduler for the PA-8000. Several features of the micro-architecture are presented along with the heuristics used in the production compiler to model that feature. These features include latency, resource constraints, instruction polarity cache interfaces, and memory dependences ...

Keywords: HP PA-8000, compiler optimization, instruction polarity cache interfaces, instruction scheduling, latency, memory dependences, micro-architecture, production compiler, resource constraints, scheduling

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L2	60	sonar and dsp and multipath	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/09 11:51
L3	0	2 and z-domain	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/09 11:48
L4	14	hardware and stage and z-domain and latency	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/09 13:06
L5	1	"4689762".PN.	USPAT; USOCR	OR	OFF	2005/06/09 13:05
L6	1	"4831570".PN.	USPAT; USOCR	OR	OFF	2005/06/09 13:05
L7	13195875	(no or "not" or without) near/3 latency	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/09 13:07
L8	3371	(no or "not" or without) near3 latency	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/09 13:07
L9	2569	(no or "not" or without) adj3 latency	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/09 13:07
L10	2569	(no or without) adj3 latency	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/09 13:07

L11	1788	(no or without) adj2 latency	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/09 13:07
L12	68	11 same stage	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/09 13:10
L13	19	(without adj regard adj2 latency)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/09 13:10
L14	1	"5128871".PN.	USPAT; USOCR	OR	OFF	2005/06/09 13:22
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... Software's products and the ability of Ascential Software's products to locate, standardize and integrate risk- bearing data **without regard to source or latency** ...
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Broadband satellite internet access high speed satellite internet ... [distance: 2]

... but essentially it is a product of hardware in the network that allows TCP/IP to function **without regard to the latency** in the connection. ...
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Instruction Scheduling for the HP PA-8000 [distance: 1]

tions in the same cycle **without regard for latency**. Notice. that this schedule could retire ... that scheduling **without regard for latency** is reasonable for ...
<http://doi.ieeecomputersociety.org/10.1109/MICRO.1996.566470> - - [Cached](#) - [Similar pages](#)

Multi-media analog/digital/optical switching apparatus - US Patent ... [distance: 1]

Secondly, they are generally switching systems designed to switch high bandwidth serial transfers or analog transmissions **without regard for latency**. ...

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... a cause of the occupational disease, **without regard to the latency period** for the exposure or whether the employer was the last employer of the worker. ...

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OFTC #kernelnewbies DejaIRC 15 August 2002, 14:30-15:30 [distance: 2]

... users sending in requests as fast as possible, **without regard for request latency**, and measure only how many request/minute the server completes ...

<http://ds9a.nl/klogbot/?year=2002&month=8&day=15&hour=15> - 18k - [Cached](#) - [Similar pages](#)

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... operations. With sequential I/O, it is possible to run a 9-GB disk drive

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Redmond | Feature Article: Remote Operator [distance: 3]

... OS services, and applications **without regard for bandwidth, latency**, or the failure of a single network interface anywhere along the connection path. ...

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Role and limitations of epidemiology in establishing a causal ... [distance: 1]

(or marker) **without regard to latency**. Case-control studies. Correlation between exposure and disease. (or marker) with improved understanding ...

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THINC: A Remote Display Architecture for Thin-Client Computing [distance: 1]

networks, **without regard for latency** and responsiveness. Still, a growing number of ASPs such as services from FutureLink ...

<http://www.ncl.cs.columbia.edu/publications/cucs-027-04.pdf> - - [Cached](#) - [Similar pages](#)

TO: CCJ Board Members FROM Peter Guzzo DATE: March 5, 2002 SUBJECT ... [distance: 2]

... a cause of the occupational disease, **without regard to the latency period** for the exposure or whether the employer was the last employer of the worker. ...

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The price of anarchy is independent of the network topology [distance: 3]
... latency) is minimized; however, in many settings network users are free to route their traffic in a selfish manner, **without regard to the total latency**. ...
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Tim Roughgarden [distance: 3]
... latency) is minimized; however, in many settings network users are free to route their traffic in a selfish manner, **without regard to the total latency**. ...
http://www.cs.cornell.edu/courses/cs789/2002sp/tim_roughgarden.htm - 5k - [Cached](#) - [Similar pages](#)

United States Patent Application: 0020010897 [distance: 1]
... is characterized by constant latency, the method comprising: a) specifying said transfer function; b) **without regard to latency** characteristics, specifying a ...
<http://appft1.uspto.gov/netacgi/nph-Parser?Sect1=PTO2&Sect2=HITOFF&p=1&u=%2Fnetacgi%2FPTO%2Fsearch-bool.html&i=13&f=G&i=50&co1=AND&d=PG01&s1=walstein&OS=walstein&RS=walstein> - 39k - [Cached](#) - [Similar pages](#)

We study the degradation in network performance caused by the ... [distance: 3]
... however, in many settings network users are free to route their traffic in a selfish manner, **without regard to the total latency**. ...
<http://theory.stanford.edu/~tim/abstracts/indep.txt> - 4k - [Cached](#) - [Similar pages](#)

Web Services Part 4: Offering Enterprise Web Services [distance: 2]
... transparent. That is, all invocations were handled **without regard to network latency**, often the biggest bottleneck in a distributed system. ...
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Welcome to NJRMA [distance: 2]
... that employer or those employers, exposed to a deleterious substance which is a cause of the occupational disease, **without regard to the latency** period for the ...
<http://njrma.org/members/Publications/State%20House%20Watch/SHW%205-16-03.htm> - 54k - [Cached](#) - [Similar pages](#)

WhitePaper:PhysicalStorageBestPracticesfor Cisco Unity 4.0 (With ... [distance: 3]
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nickel, epidemiological studies: International Committee on Nickel ... [distance: 1]
the lung and nose, the risks were estimated **without regard for latency**. - Exposure during the last five years prior to death was omitted from the analysis ...
<http://cdrc00.rug.ac.be/healthrisk/Nickel/literature/rep%20int%20committee%20Ni.5der.htm> - 225k - [Cached](#) - [Similar pages](#)

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record for this subject, **without regard to latency**. The average of class 4(μ . 4.), showed large activity in. the anterior area, which seems to be caused ...
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